



UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
United States Patent and Trademark Office
Address: COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, Virginia 22313-1450
www.uspto.gov

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
-----------------	-------------	----------------------	---------------------	------------------

10/787,380

02/26/2004

Brad L. Grande

2-1

4270

47386 7590 02/04/2009
RYAN, MASON & LEWIS, LLP
1300 POST ROAD
SUITE 205
FAIRFIELD, CT 06824

EXAMINER

GETACHEW, ABIY

ART UNIT

PAPER NUMBER

2841

MAIL DATE

DELIVERY MODE

02/04/2009

PAPER

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

Office Action Summary	Application No. 10/787,380	Applicant(s) GRANDE ET AL.	
	Examiner ABIY GETACHEW	Art Unit 2841	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 11 November 2008.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-20 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1-20 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 26 February 2004 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|--|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413) |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | Paper No(s)/Mail Date. _____ |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO/SB/08) | 5) <input type="checkbox"/> Notice of Informal Patent Application |
| Paper No(s)/Mail Date _____ | 6) <input type="checkbox"/> Other: _____ |

DETAILED ACTION

Claim Rejections - 35 USC § 103

1. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

2. The text of those sections of Title 35, U.S. Code not included in this action can be found in a prior Office action.

3. This application currently names joint inventors. In considering patentability of the claims under 35 U.S.C. 103(a), the examiner presumes that the subject matter of the various claims was commonly owned at the time any inventions covered therein were made absent any evidence to the contrary. Applicant is advised of the obligation under 37 CFR 1.56 to point out the inventor and invention dates of each claim that was not commonly owned at the time a later invention was made in order for the examiner to consider the applicability of 35 U.S.C. 103(c) and potential 35 U.S.C. 102(e), (f) or (g) prior art under 35 U.S.C. 103(a).

4. Claims 1-20 are rejected under 35 U.S.C. 103(a) as being unpatentable over Arnett et al. (US 6,078,661) hereafter refer as Arnett in view of Laity (US 5,984,731).

Regarding claims 1 and 8, Arnett disclose a modem module (See figure 2 element 10) for connecting to a carrier assembly [Column 2 lines 57-67], comprising: circuitry for interfacing with a telephone line (figure 2 element 25, wire modular connectors for phone lines)

Arnett does not expressly disclose one or more solder pads for connecting a signal line of said modem module to said carrier assembly.

Laity discloses one or more solder pads for connecting a signal line of said modem module to said carrier assembly. (See figure 7, i.e. the card connector 34 is mounted along a forward margin 172 of the PCBA 160, the leads 42 from the connector 34 being soldered to a terminal section along the forward margin 172)

At the time of the invention, it would have been obvious to a person of ordinary skill in the art to modify the components shown schematically in figure 7 of Laity's to incorporate with Arnett's printed circuit board in order to mounting and soldered various kinds of electronic components to a circuit board.

Regarding claim 2, as modified above Arnett disclose a tip/ring (figure 2 element 24) connector for interfacing with said telephone line (figure 2 element 25).

Regarding claim 3, as modified above Arnett disclose a connection to a tip/ring connector (figure 2, element 24). (See figure 2)

Regarding claim 4, as modified above Arnett disclose wherein said carrier assembly is a motherboard (see figure 2 element 22, i.e. motherboard is the central or primary circuit board making up a complex electronic system, such as a modern computer).

Regarding claim 5, as modified above Arnett disclose wherein said one or more solder pads are soldered to corresponding one or more solder pads (See figure 2 element 45) on said carrier assembly (figure 2 element 24).

Regarding claim 6, as modified above Arnett disclose wherein said modem

Art Unit: 2841

assembly is fabricated on a printed circuit board (see figure 2 element 22, i.e. motherboard is the central or primary circuit board making up a complex electronic system, such as a modern computer).

Regarding claim 7, as modified above Arnett disclose wherein said modem assembly is an integrated device (See the Abstract, i.e. a network interface device includes a circuit board secured in the base, Integrated device defined as an electronic circuit-consisting of components and connectors-contained on a semiconductor chip)

Regarding claim 9, as modified above Arnett disclose a tip/ring (figure 2 element 24) connector for interfacing with said telephone line (figure 2 element 25).

Regarding claim 10, as modified above Arnett disclose a connection to a tip/ring connector (figure 2, element 24). (See figure 2)

Regarding claim 11, as modified above Arnett disclose wherein said carrier assembly is a motherboard (see figure 2 element 22, i.e. motherboard is the central or primary circuit board making up a complex electronic system, such as a modern computer).

Regarding claim 12, as modified above Arnett disclose wherein said one or more solder pads are soldered to corresponding one or more solder pads (See figure 2 element 45) on said carrier assembly (figure 2 element 24).

Regarding claim 13, as modified above Arnett disclose wherein said modem assembly is fabricated on a printed circuit board (see figure 2 element 22, i.e. motherboard is the central or primary circuit board making up a complex electronic system, such as a modern computer).

Regarding claim 15, as modified above Arnett disclose a tip/ring (figure 2 element 24) connector for interfacing with said telephone line (figure 2 element 25).

Regarding claim 16, as modified above Arnett disclose a connection to a tip/ring connector (figure 2, element 24). (See figure 2)

Regarding claim 17, as modified above Arnett disclose wherein said carrier assembly is a motherboard (see figure 2 element 22, i.e. motherboard is the central or primary circuit board making up a complex electronic system, such as a modern computer).

Regarding claim 18, as modified above Arnett disclose wherein said one or more solder pads are soldered to corresponding one or more solder pads (See figure 2 element 45) on said carrier assembly (figure 2 element 24).

Regarding claim 19, as modified above Arnett disclose wherein said modem assembly is fabricated on a printed circuit board (see figure 2 element 22, i.e. motherboard is the central or primary circuit board making up a complex electronic system, such as a modern computer).

Regarding claim 20, as modified above Arnett disclose wherein said modem assembly is an integrated device. (Figure 2 element 24, wire modular connectors for phone lines, also known as registered jacks).

Response to Arguments

5. Applicant's arguments with respect to claims 1-20 have been considered but are moot in view of the new ground(s) of rejection.

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to ABIY GETACHEW whose telephone number is (571)272-6932. The examiner can normally be reached on Monday to Friday 8Am to 4:30Pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, DEAN REICHARD can be reached on (571)272-1984. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

/Jeremy C. Norris/
Primary Examiner, Art Unit 2841

Abiy Getachew
Examiner
Art Unit 2841

A.G.
January 29, 2009